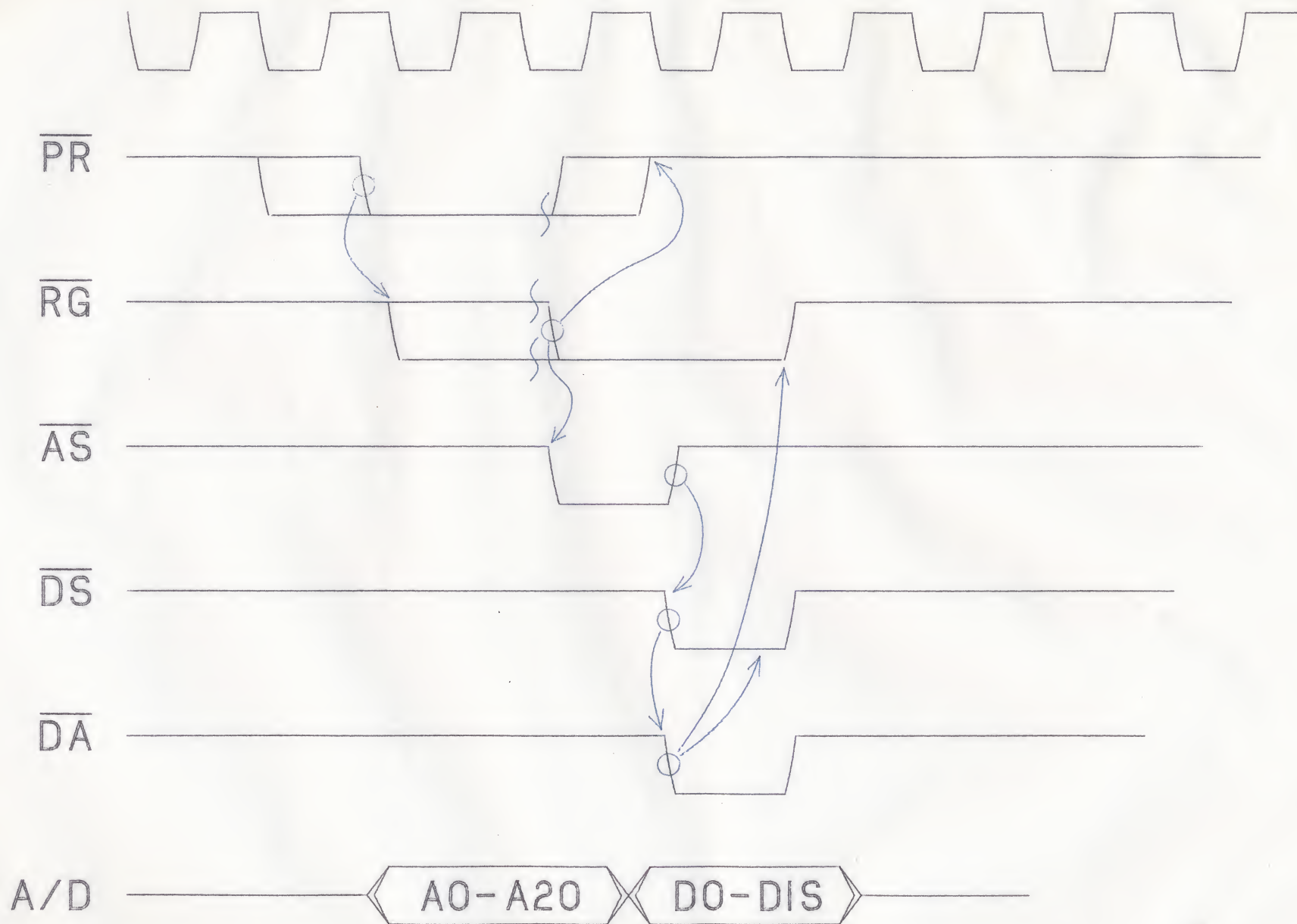
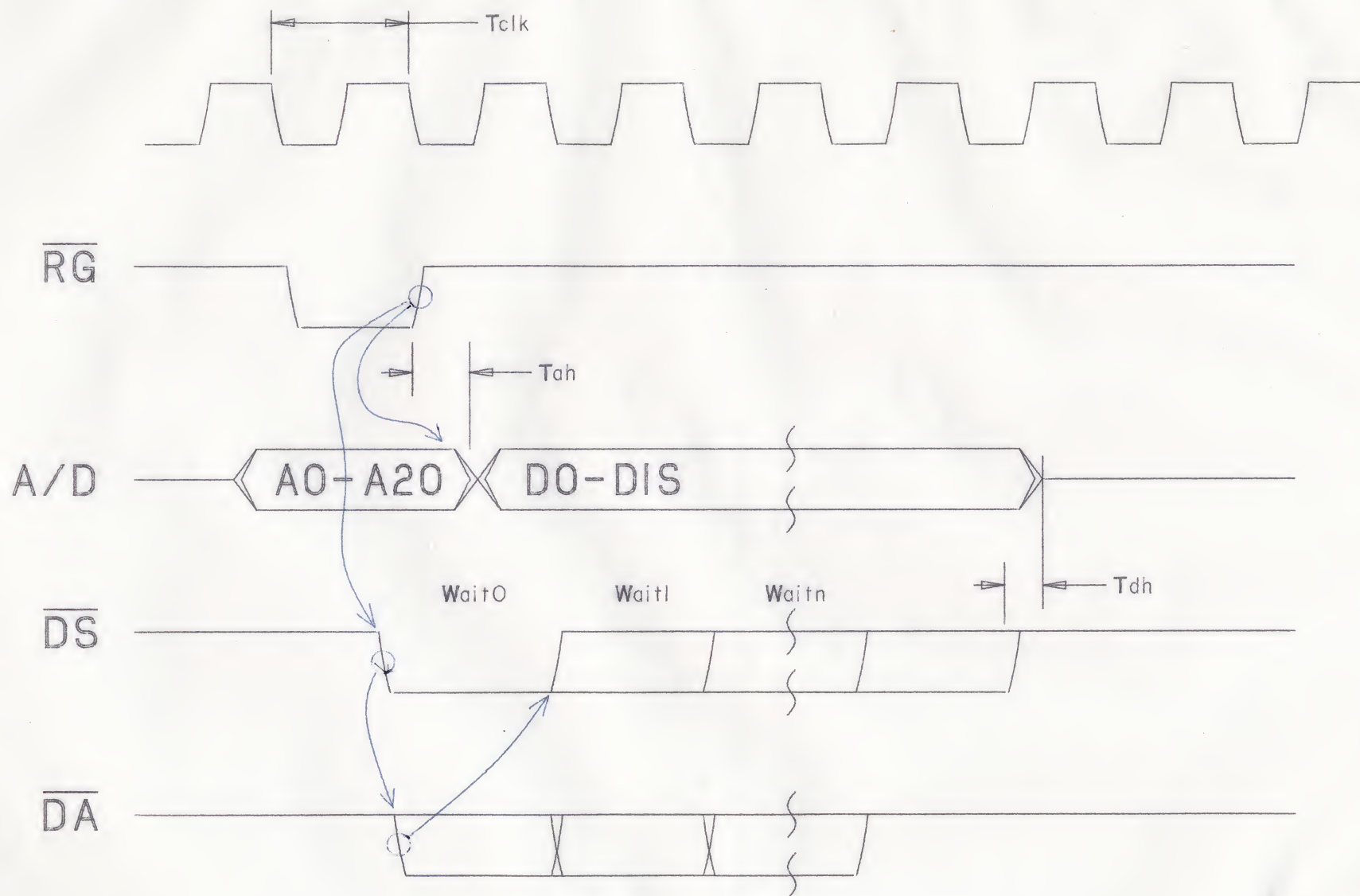


SILVER CHIP - PIXEL DATA. PARAMETER LOAD MEMORY CYCLE

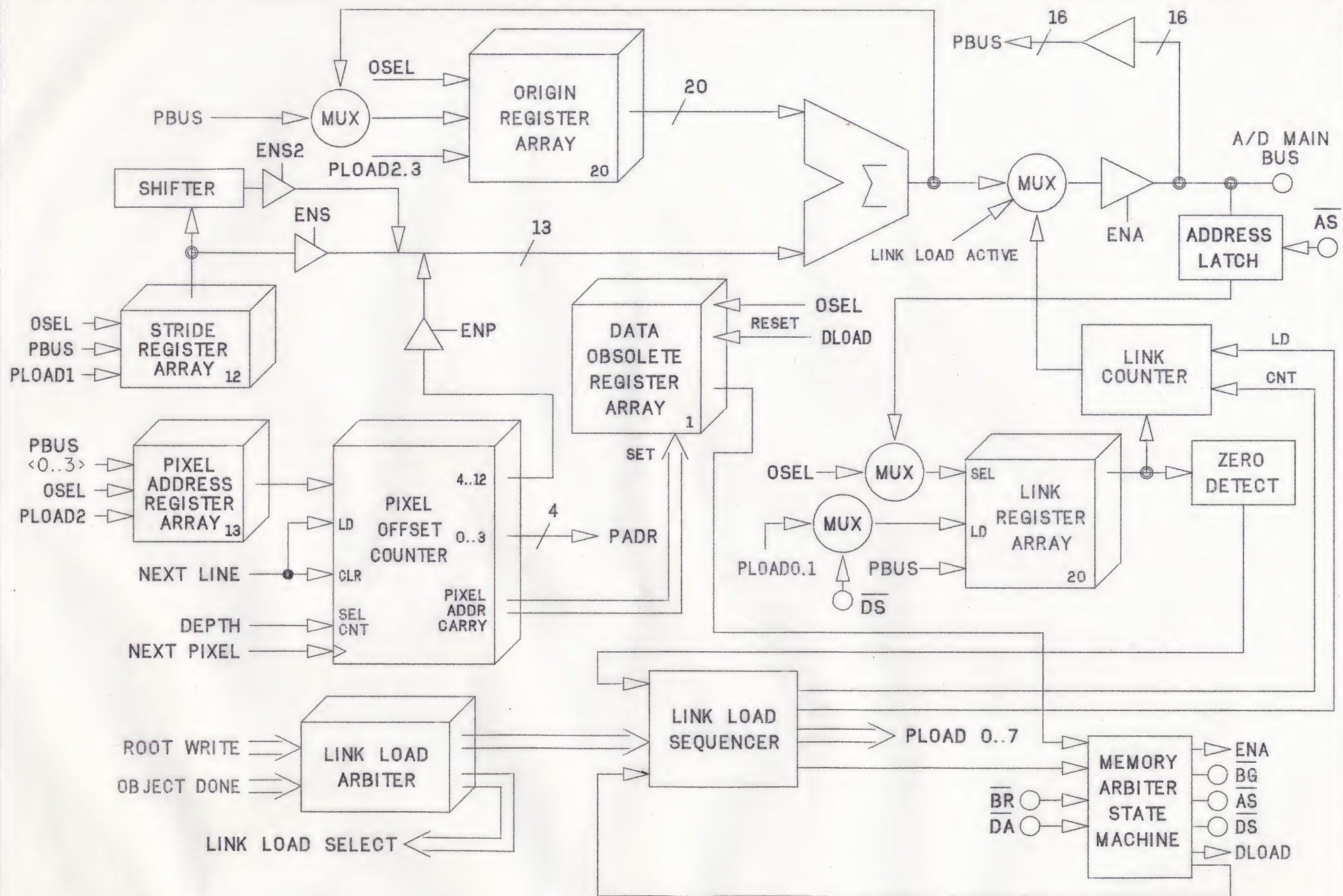


SILVER CHIP - ROOT WRITE CYCLE

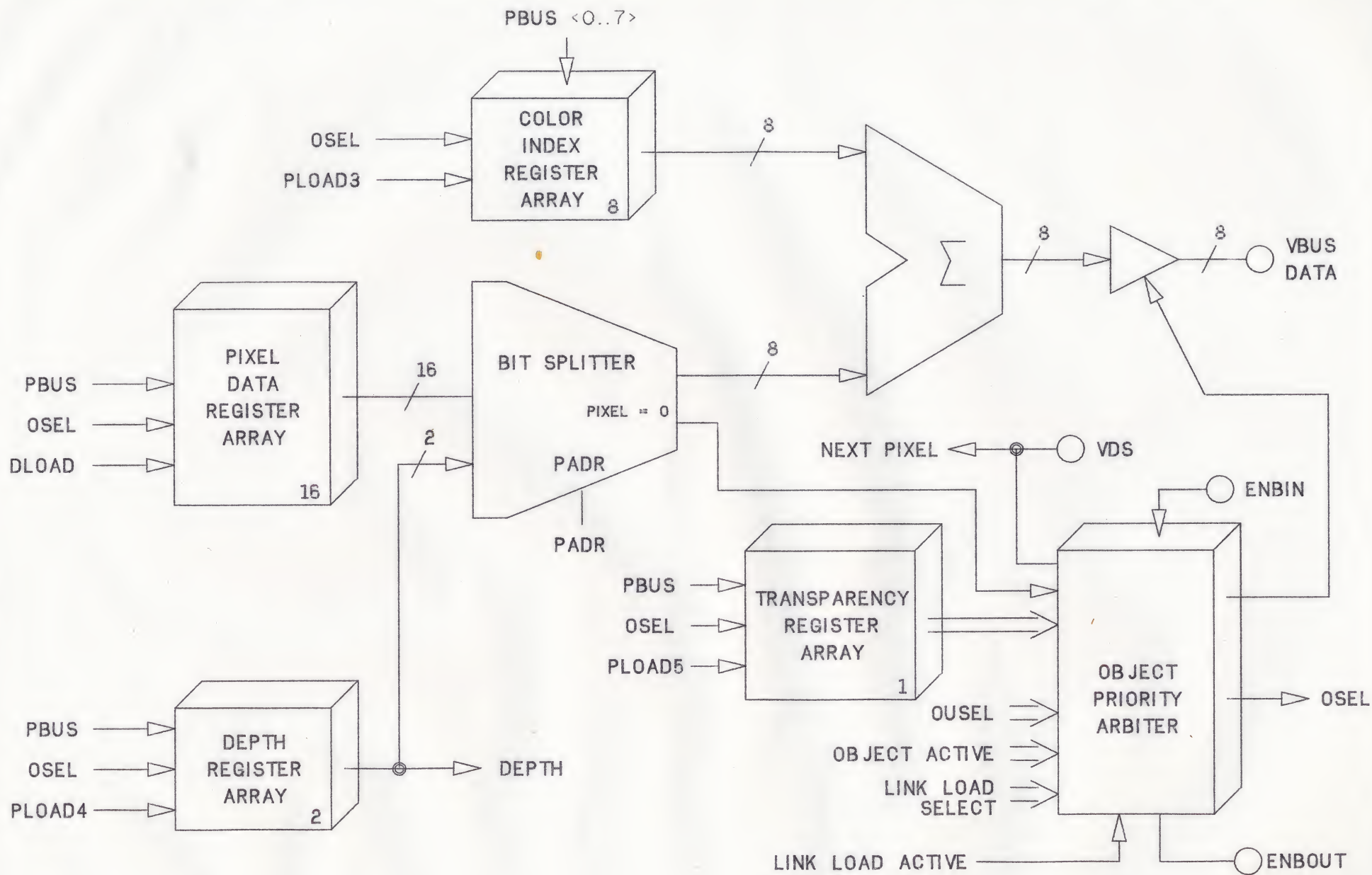


SILVER CHIP - PIXEL DATA, PARAMETER LOAD MEMORY CYCLE



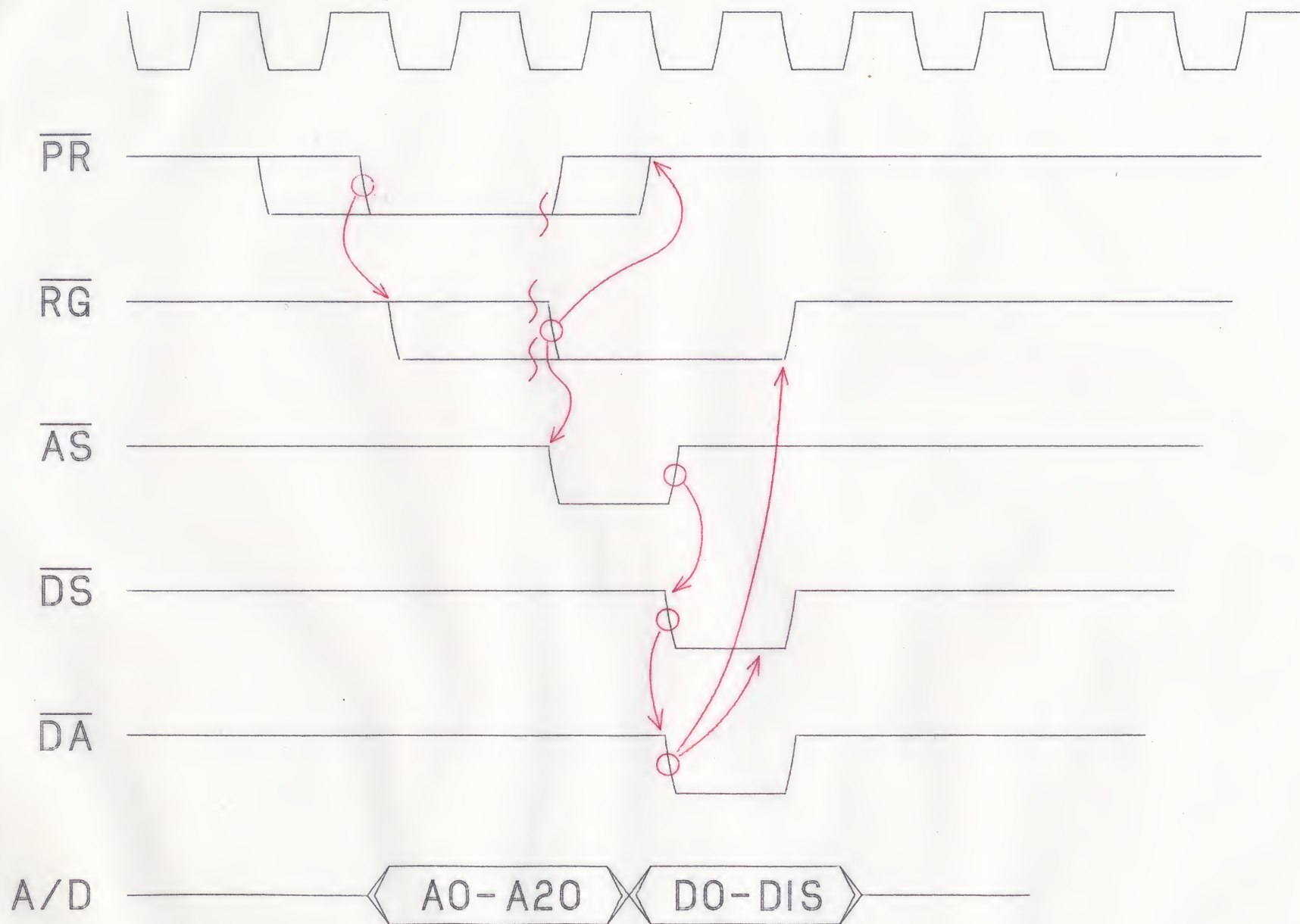


# ADDRESS CONTROLLER

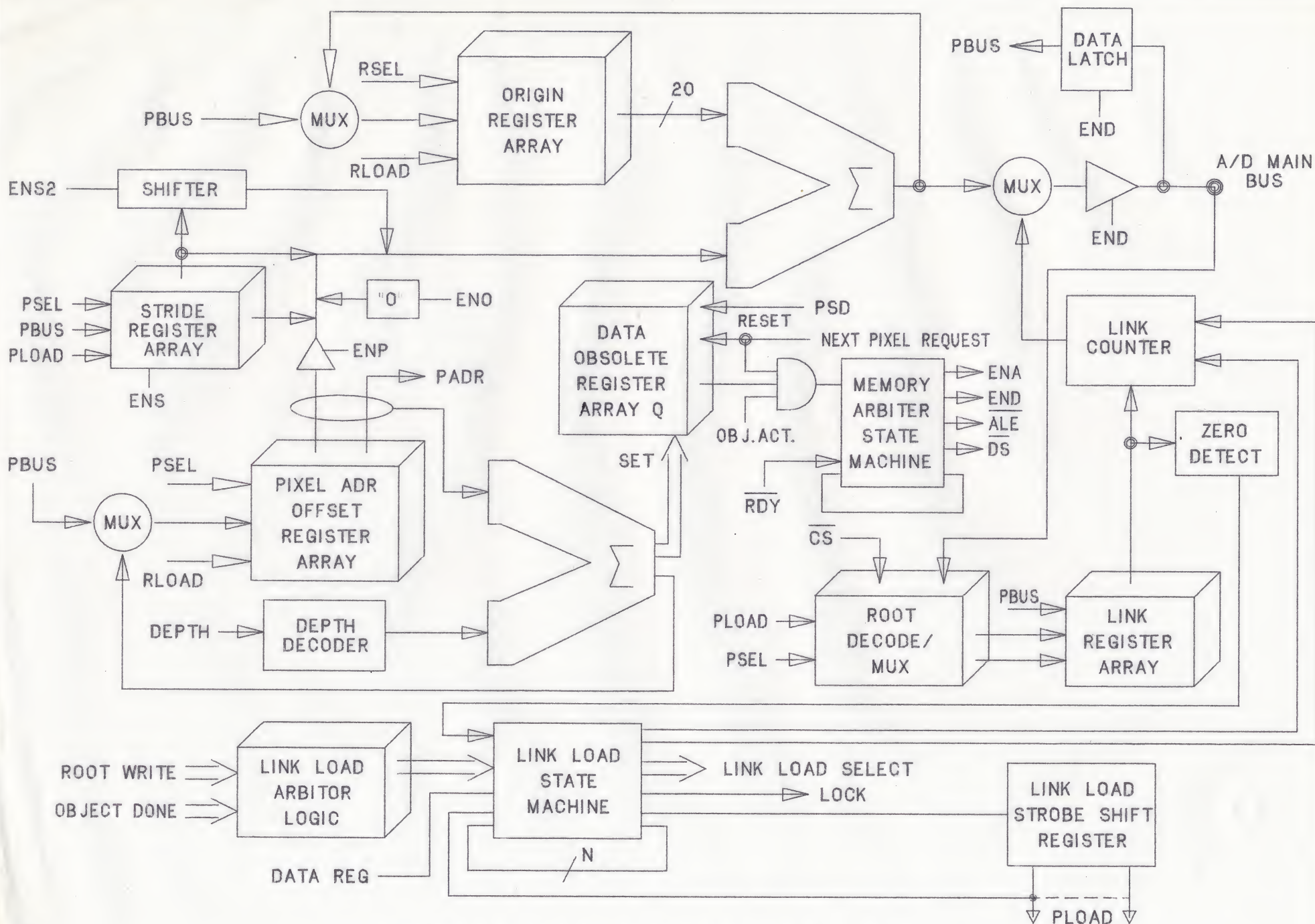


# PIXEL PROCESSOR



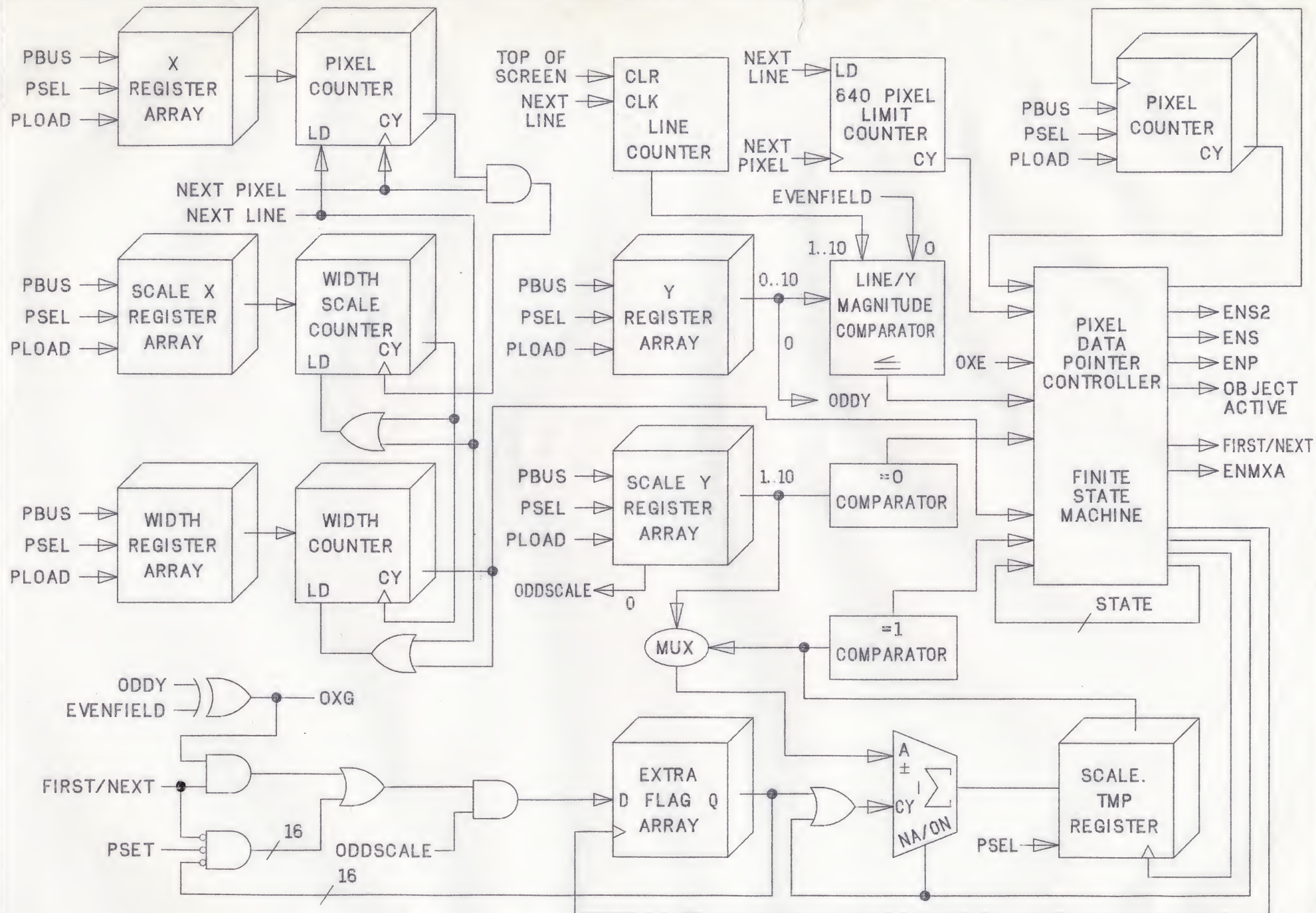


SILVER CHIP - ROOT WRITE CYCLE



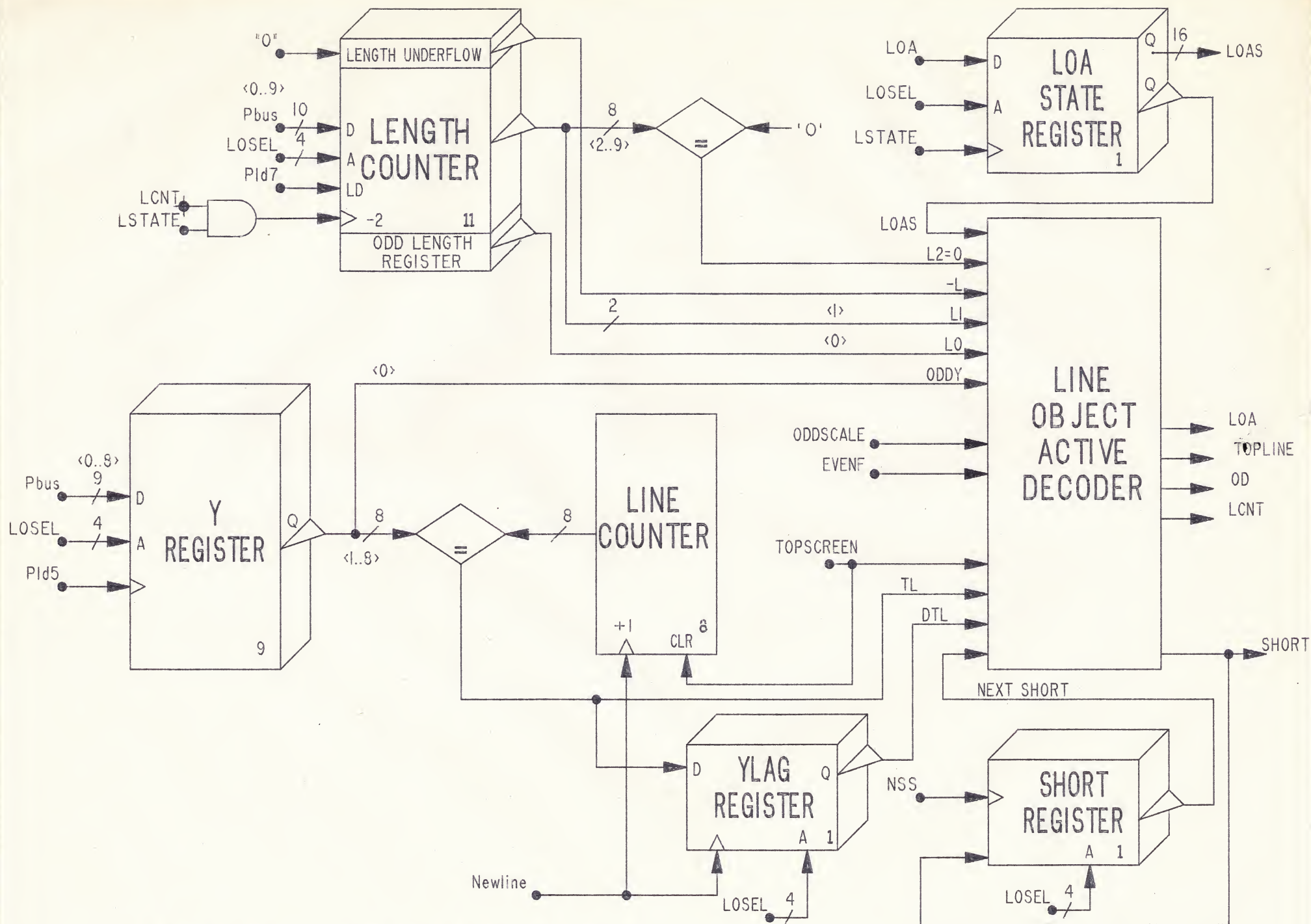
# ADDRESS CONTROLLER





OBJECT ACTIVE CIRCUIT

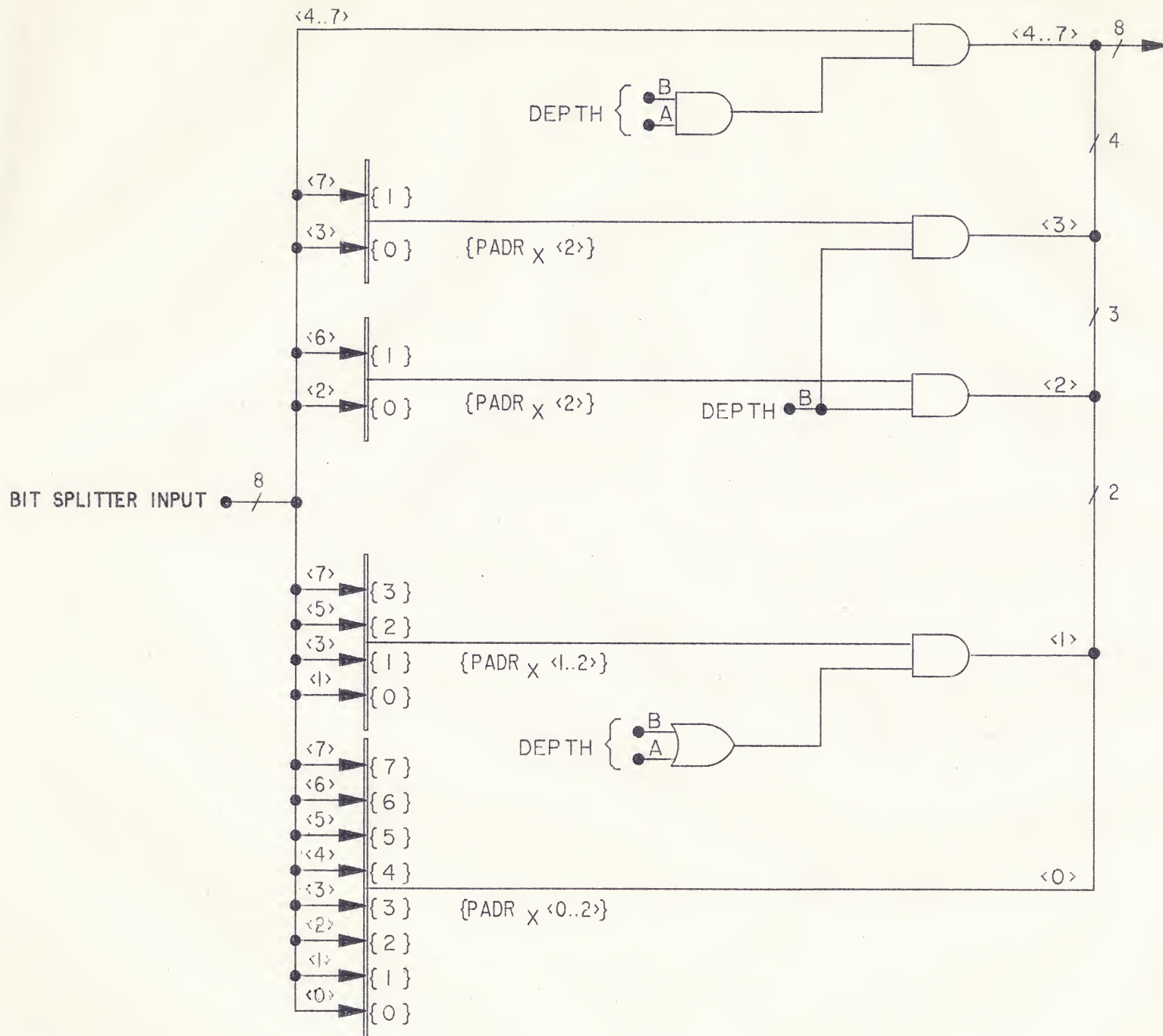




LINE OBJECT ACTIVE

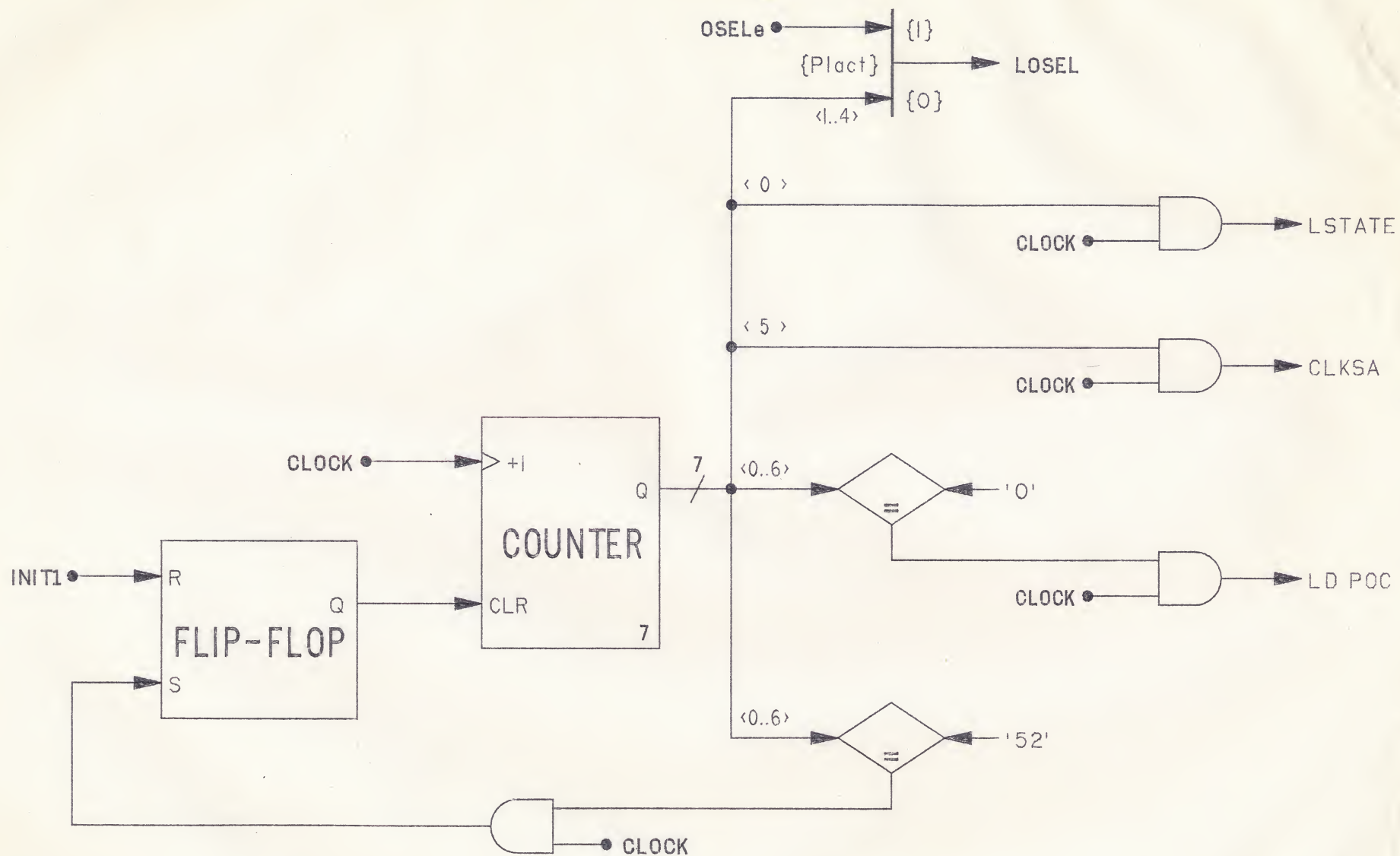
7-27-83 RAINBOW

A.L. 7-27-83 CD.RAINBOW.LINE-OBJECT-ACTIVE

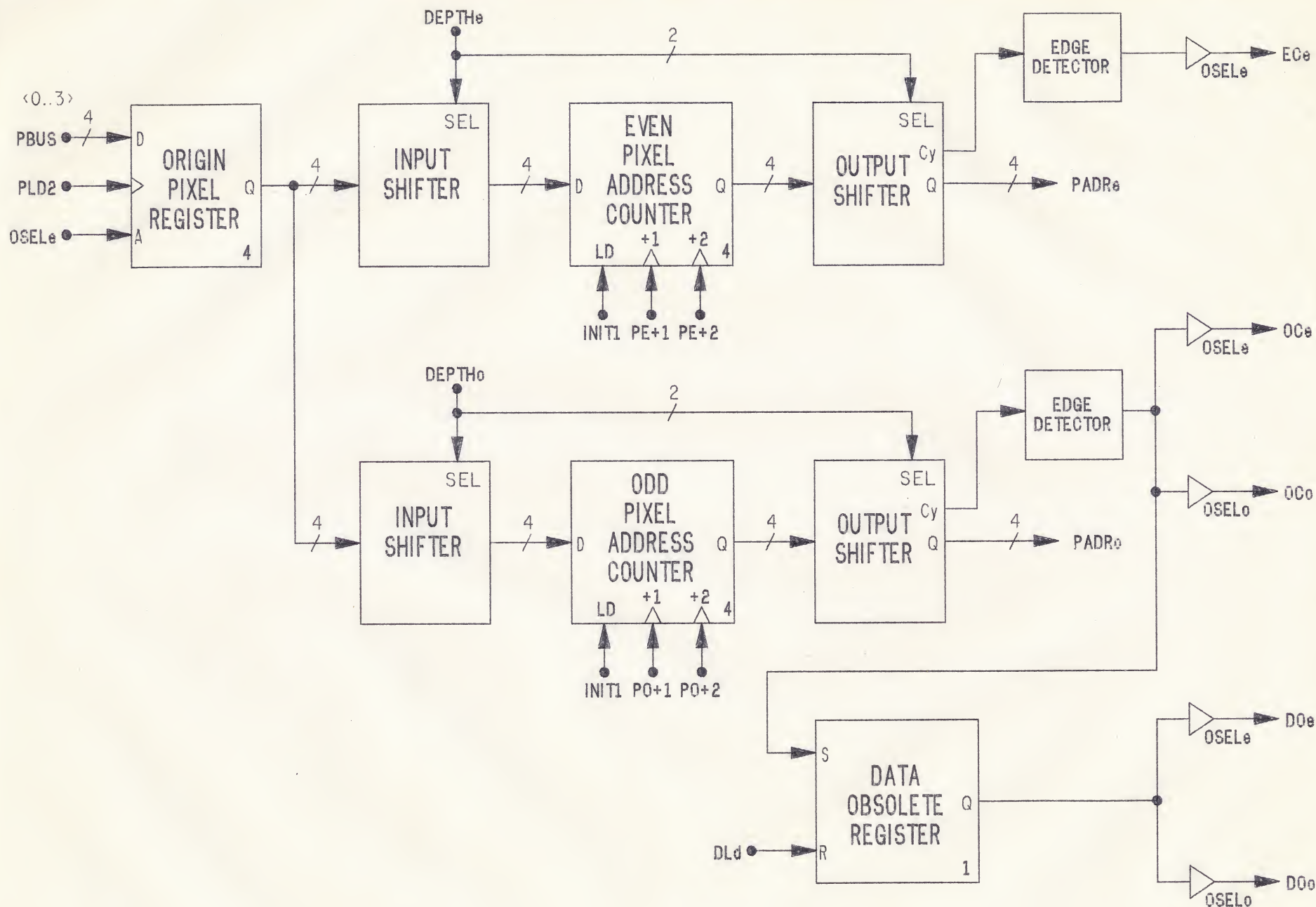


BIT-SPLITTER



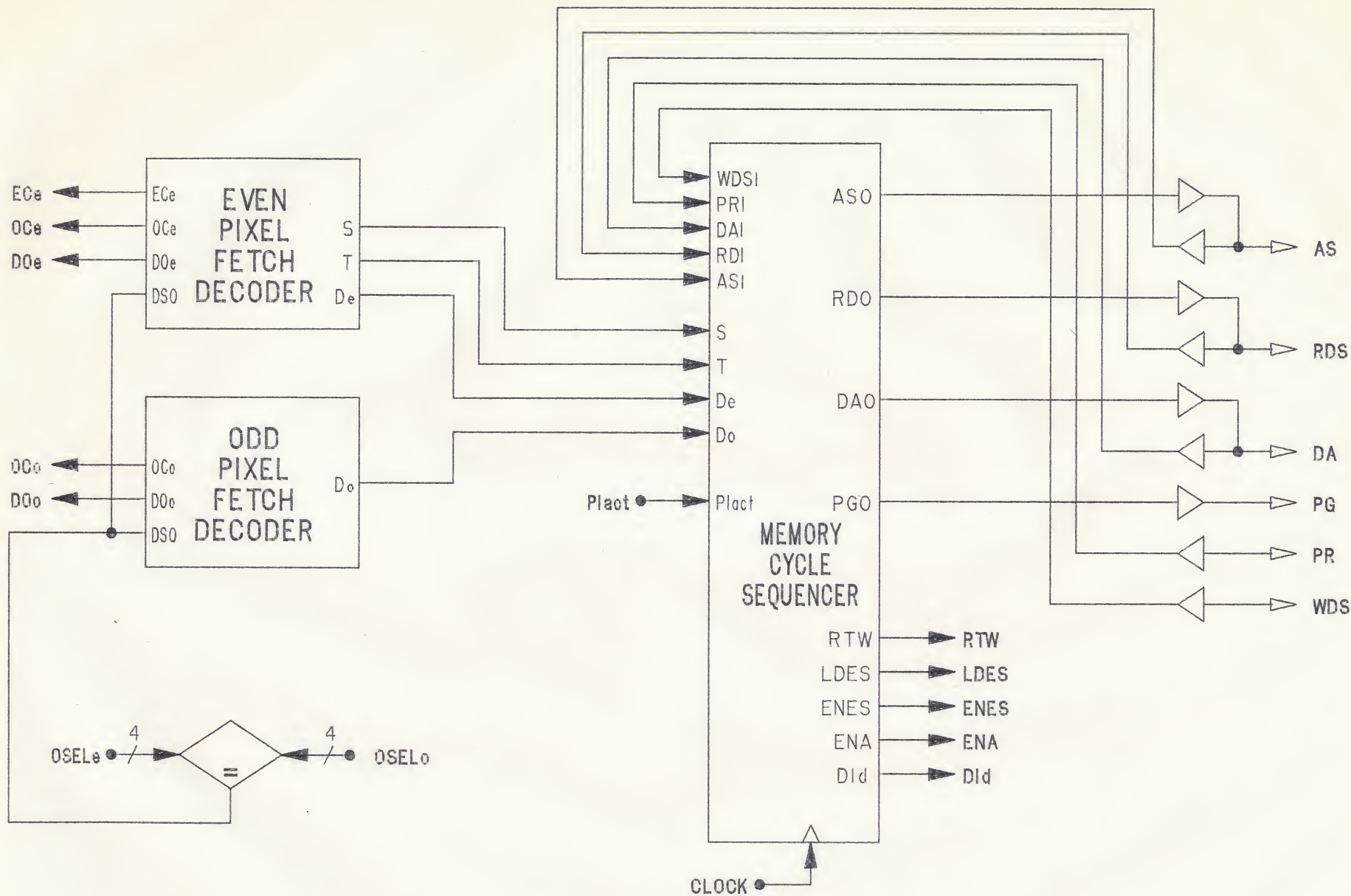


LINE OBJECT SEQUENCER

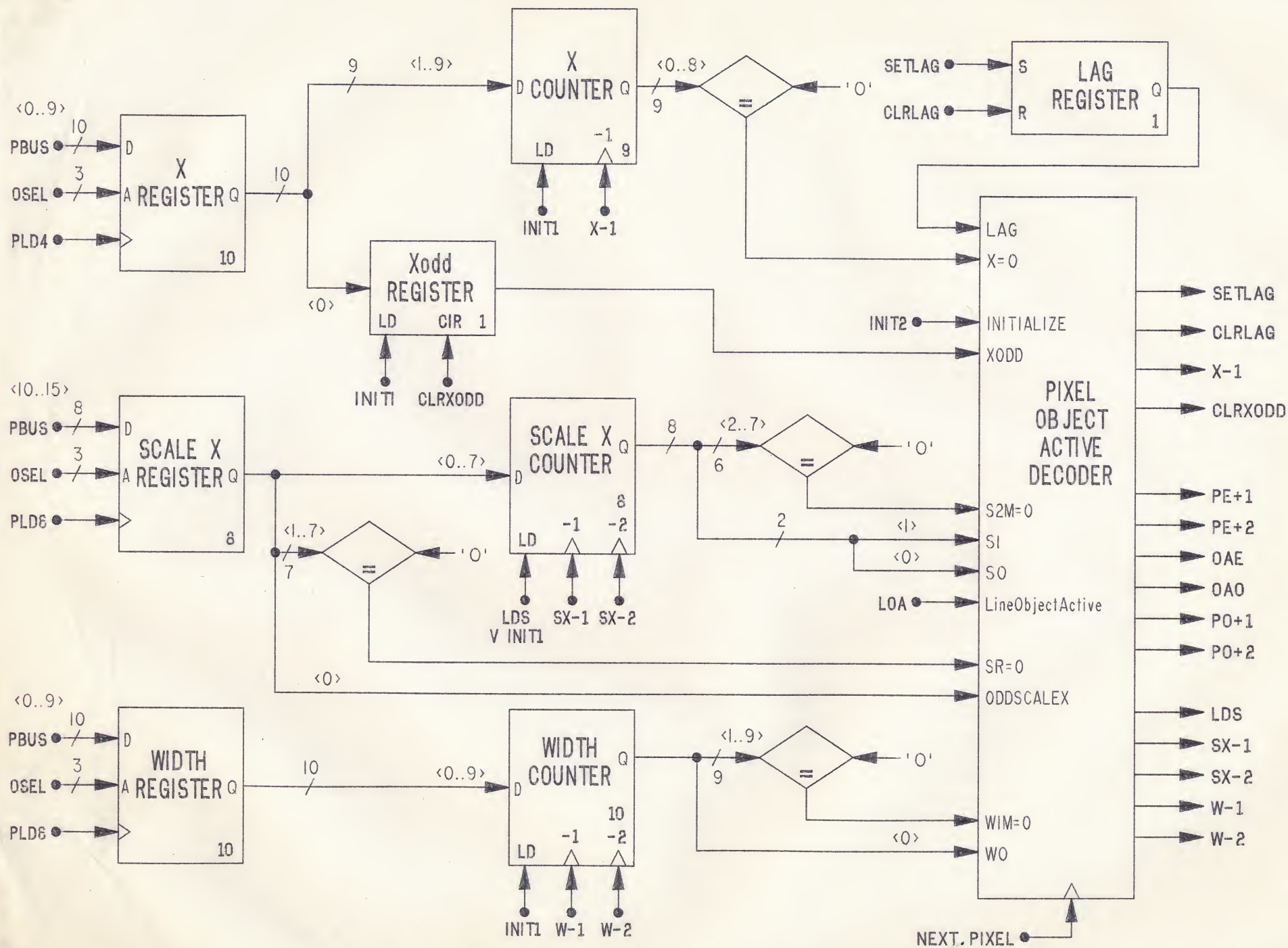


PIXEL ADDRESS COUNTER (per object)





# MEMORY CYCLE LOGIC

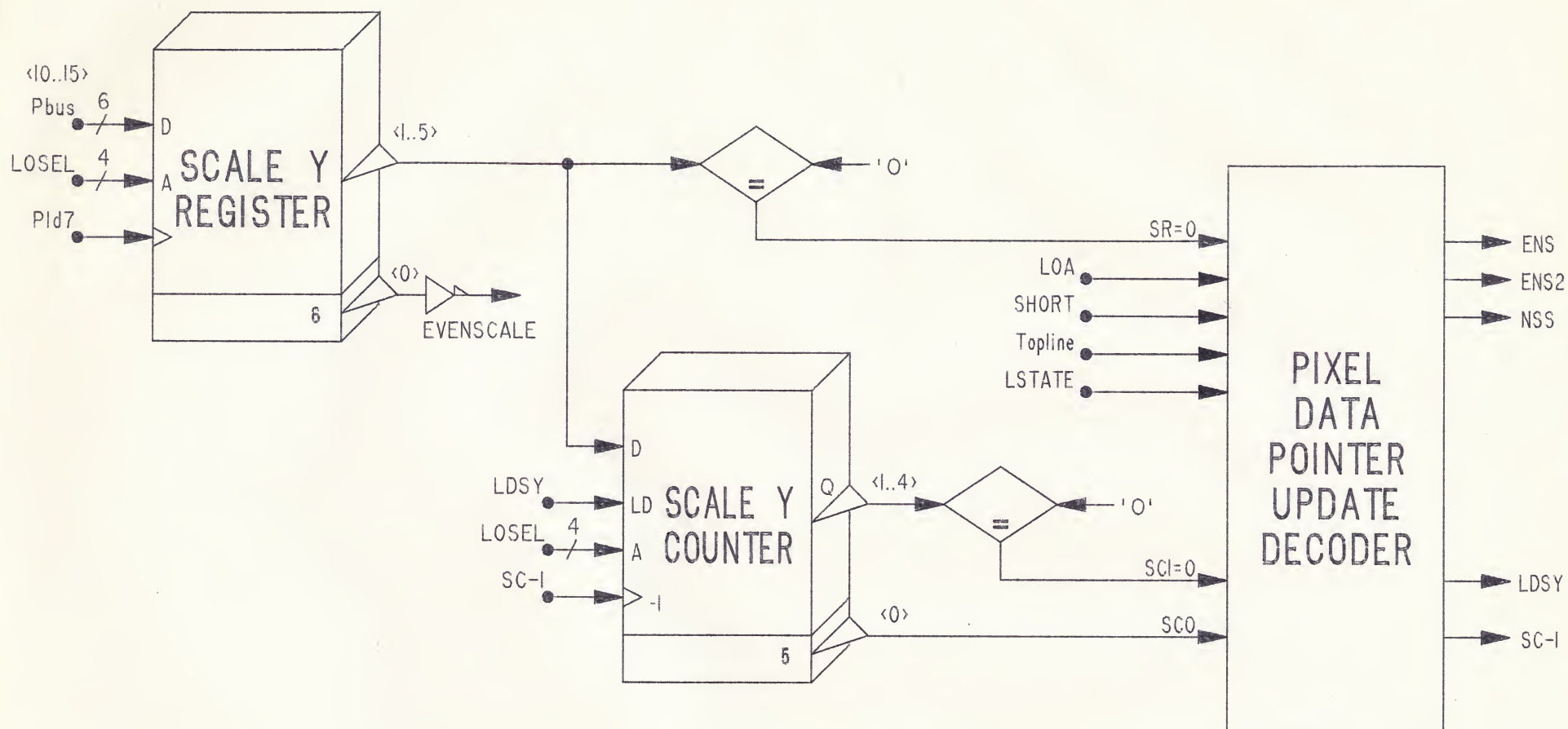


PIXEL OBJECT ACTIVE (per object)

6-7-83 RAINBOW

J.J. 7-30-83 CD.RAINBOW.PIXEL-ACTIVE

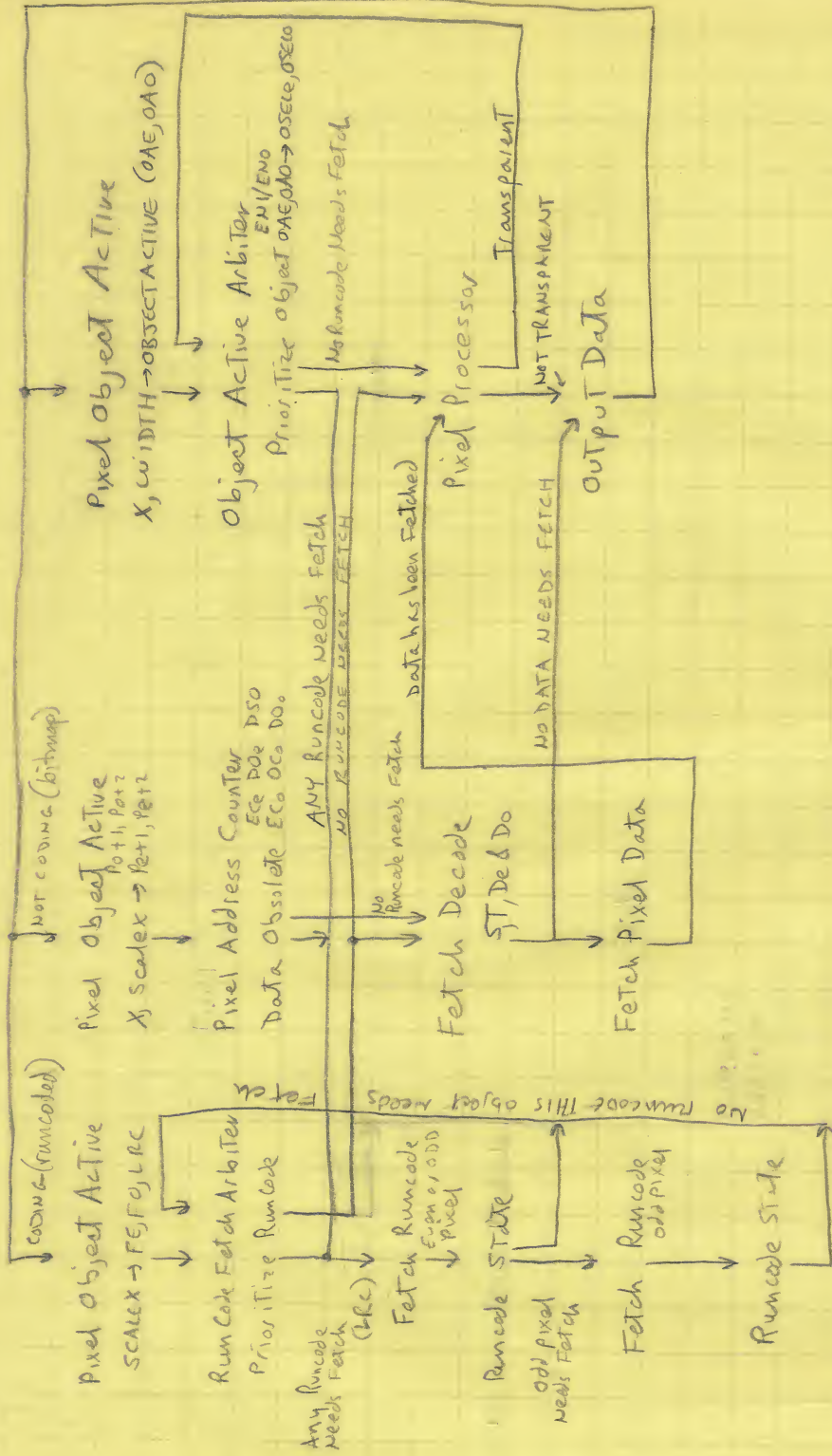




Y SCALE LOGIC







Silver IC Pipeline Flow Chart

11/2  
8/25/83

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